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Collective Mining Ltd. is pleased to provide an exploration update on its Guayabales Project located in Caldas, Colombia. The project is situated contiguous, immediately along strike and to the ...

~~Collective Mining Channel Samples 45.52 g/t gold and 27 g/t silver along 37.5 metres in Underground Development at the Guayabales Project, Colombia~~

The model EV12AQ600 ADC (see image above) features 12-bit resolution and 1.6 GS/s maximum sampling rate across each channel or a maximum rate of 6.4 GS/s for one channel when the four channels are ...

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~~Four-Channel ADC Makes the Grade for Space~~

TI extends its family of SAR ADCs, striving to beat old design challenges. How do these new family members stack up when compared to some competitors?

~~TI 's New SAR ADCs Beat Old Noise and Sampling Rate Challenges~~

Electronic enthusiasts may be interested in a new open source, stackable 32-channel EEG ADC powered by a Cortex-M7 which has just launched via the Crowd Supply website and is now available to ...

~~Free EEG 32 open source 32-channel EEG ADC~~

Battery Mineral Resources Corp. (TSXV: BMR) (" Battery " or " BMR " or the " Company ") announces that it has completed a seven hole - 682.00 metre diamond drill program on its 100% owned Bald Rock ...

~~Battery Mineral Resources Plans Diamond Drilling on the Bald Rock Cobalt Silver Target in the Gowganda Area~~

EG-UC5T is characterised by its large, 4.0-mm instrument channel and its short rigid distal ... not only able to perform the adequate tissue sampling, but also various therapeutic interventional ...

~~SonoScape obtains CE Mark for its linear array echoendoscope EG-UC5T~~

Scottie Resources Corp. (" Scottie " or the " Company ") (TSXV: SCOT) is happy to report that it has commenced its 2021 drill program. The 12,500 m drill program is designed to advance multiple ...

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~~Scottie Commences 2021 Drill Program and Reports New Intercept of 6.2 g/t Gold Over 4.35 m at Blueberry Zone~~

MacDonald Mines Exploration Ltd. ("MacDonald Mines" or the "Company") announces results from 3 further channel samples taken at the Glade West Showing as part of its ongoing mechanized stripping and ...

~~MacDonald Mines Reports 8.91 g/t Gold over 1.60 m, including 17.50 g/t Gold over 0.80 m from Further Channel Samples at Glade~~

The company noted significant high-grade mineralisation ... 0.2% zinc including; 4.0m at 203.8 g/t AuEq2 – 201.6 g/t gold, 172.0 g/t silver, 0.1% zinc. Sampling up-dip of previous channel ...

~~Why the Challenger (ASX:CEL) share price is storming 27% higher today~~

Winshear Gold Corp. (TSX-V: WINS) is pleased to report that it has received gold assay results from an additional 199 bedrock channel samples taken in April and May of 2021 at the Gaban Gold Project ...

~~Winshear Discovers New Outcropping Gold Mineralisation at its Gaban Gold Project in the Puno Gold Belt, Peru~~

Further high ... 4% zinc, 2.2% lead and 39 g/t silver from 10.3 metres. Additionally, Alta Zinc uncovered more new mineralisation at Ponente West another 260 metres to the east, where channel ...

~~Alta Zinc expands mineralisation footprint at Gorno Project with high-grade zinc results~~

4.0-mm instrument channel and its short rigid distal end, pushing the boundaries of therapeutic

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excellence. With the arrival of the linear-array echoendoscope EG-UC5T, SonoScape users are not only ...

This monograph presents a reliable methodology for characterising the energy and eco-efficiency of unit manufacturing processes. The Specific Energy Consumption, SEC, will be identified as the key indicator for the energy efficiency of unit processes. An empirical approach will be validated on different machine tools and manufacturing processes to depict the relationship between process parameters and energy consumptions. Statistical results and additional validation runs will corroborate the high level of accuracy in predicting the energy consumption. In relation to the eco-efficiency, the value and the associated environmental impacts of manufacturing processes will also be discussed. The interrelationship between process parameters, process value and the associated environmental impact will be integrated in the evaluation of eco-efficiency. The book concludes with a further investigation of the results in order to develop strategies for further efficiency improvement. The target audience primarily comprises researchers and experts in the field, but the book may also be beneficial for graduate students.

This comprehensive handbook is a one-stop engineering reference. Covering data converter fundamentals, techniques, applications, and beginning with the basic theoretical elements necessary for a complete understanding of data converters, this reference covers all the latest advances in the field. This text describes in depth the theory behind and the practical design of data conversion circuits as well as describing the different architectures used in A/D and D/A converters. Details are provided on the

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design of high-speed ADCs, high accuracy DACs and ADCs, and sample-and-hold amplifiers. Also, this reference covers voltage sources and current reference, noise-shaping coding, and sigma-delta converters, and much more. The book's 900-plus pages are packed with design information and application circuits, including guidelines on selecting the most suitable converters for particular applications. You'll find the very latest information on:

- Data converter fundamentals, such as key specifications, noise, sampling, and testing
- Architectures and processes, including SAR, flash, pipelined, folding, and more
- Practical hardware design techniques for mixed-signal systems, such as driving ADCs, buffering DAC outputs, sampling clocks, layout, interfacing, support circuits, and tools.

- Data converter applications dealing with precision measurement, data acquisition, audio, display, DDS, software radio and many more. The accompanying CD-ROM provides software tools for testing and analyzing data converters as well as a searchable pdf version of the text. * Brings together a huge amount of information impossible to locate elsewhere. * Many recent advances in converter technology simply aren't covered in any other book. * A must-have design reference for any electronics design engineer or technician.

Brain-Computer Interfacing, Volume 168, not only gives readers a clear understanding of what BCI science is currently offering, but also describes future expectations for restoring lost brain function in patients. In-depth technological chapters are aimed at those interested in BCI technologies and the

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nature of brain signals, while more comprehensive summaries are provided in the more applied chapters. Readers will be able to grasp BCI concepts, understand what needs the technologies can meet, and provide an informed opinion on BCI science. Explores how many different causes of disability have similar functional consequences (loss of mobility, communication etc.) Addresses how BCI can be of use Presents a multidisciplinary review of BCI technologies and the opportunities they provide for people in need of a new kind of prosthetic Offers a comprehensive, multidisciplinary review of BCI for researchers in neuroscience and traumatic brain injury that is also ideal for clinicians in neurology and neurosurgery

Devices overview. Discrete signal and systems. Z transforms. The discrete Fourier transform. FIR and IIR filter design methods. Kalman filters. Implementation of digital control algorithms. Review of architectures. Microcontrollers. Systolic arrays. Case studies.

This book offers students and those new to the topic of analog-to-digital converters (ADCs) a broad introduction, before going into details of the state-of-the-art design techniques for SAR and DS converters, including the latest research topics, which are valuable for IC design engineers as well as users of ADCs in applications. The book then addresses important topics, such as correct connectivity of ADCs in an application, the verification, characterization and testing of ADCs that ensure high-quality end products. Analog-to-digital converters are the central element in any data processing system and regulation loops such as modems or electrical motor drives. They significantly affect the performance and resolution of a system or end product. System development engineers need to be familiar with the

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performance parameters of the converters and understand the advantages and disadvantages of the various architectures. Integrated circuit development engineers have to overcome the problem of achieving high performance and resolution with the lowest possible power dissipation, while the digital circuitry generates distortion in supply, ground and substrate. This book explains the connections and gives suggestions for obtaining the highest possible resolution. Novel trends are illustrated in the design of analog-to-digital converters based on successive approximation and the difficulties in the development of continuous-time delta-sigma modulators are also discussed.

This book focuses on key simulation and evaluation technologies for 5G systems. Based on the most recent research results from academia and industry, it describes the evaluation methodologies in depth for network and physical layer technologies. The evaluation methods are discussed in depth. It also covers the analysis of the 5G candidate technologies and the testing challenges, the evolution of the testing technologies, fading channel measurement and modeling, software simulations, software hardware cosimulation, field testing and other novel evaluation methods. The fifth-generation (5G) mobile communications system targets highly improved network performances in terms of the network capacity and the number of connections. Testing and evaluation technologies is widely recognized and plays important roles in the wireless technology developments, along with the research on basic theory and key technologies. The investigation and developments on the multi-level and comprehensive evaluations for 5G new technologies, provides important performance references for the 5G technology filtering and future standardizations. Students focused on telecommunications, electronic engineering, computer science or other related disciplines will find this book useful as a secondary text. Researchers and professionals working within these related fields will also find this book useful as a reference.

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This Special Issue focuses on the state-of-the-art results from the definition and design of filters for low- and high-frequency applications and systems. Different technologies and solutions are commonly adopted for filter definition, from electrical to electromechanical and mechanical solutions, from passive to active devices, and from hybrid to integrated designs. Aspects related to both theoretical and experimental research in filter design, CAD modeling and novel technologies and applications, as well as filter fabrication, characterization and testing, are covered. The proposed research articles deal with different topics as follows: Modeling, design and simulation of filters; Processes and fabrication technologies for filters; Automated characterization and test of filters; Voltage and current mode filters; Integrated and discrete filters; Passive and active filters; Variable filters, characterization and tunability.

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